

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Om P. Agrawal; Bai Nguyen; Kuang Chi; Brad Sharpe-Geisler; Giap Tran

Assignee: Lattice Semiconductor Corporation

Title: Scalable Serializer-Deserializer Architecture
And Programmable Interface

Serial No.: 10/619,645 Filing Date: 07/14/2003

Examiner: Jason Crawford Group Art 2819

Unit:

Docket No.: M-15135 US Confirmation 9559
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Irvine, California
March 20, 2006

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

EXHIBIT C FOR37 C.F.R. 1.131 DECLARATION

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April 7, 2003

Om P. Agrawal
Lattice Semiconductor Corp.

VIA EMAIL

Re: U.S. Patent Application entitled: "Scalable Serializer-Deserializer Architecture and Programmable Interface"

Inventor(s): Om P. Agrawal et al.

Our Reference: M-15135 US

Your Reference: L02-031

Dear Om

Enclosed for review is a draft of the above-identified patent application.

Please review this draft for technical accuracy and completeness of disclosure. We must completely, clearly and accurately describe the invention so as you review this application, please feel free to revise and supplement the description. If there are portions of this draft that are incorrect, please note them. Please also note any blank lines and/or questions located throughout the specification, which must be filled in or answered by you.

It is particularly important that at the time of filing we completely and accurately describe your invention in the application, since we cannot later add new matter to clarify or further describe the invention without running the risk of having to file another application which would lose the benefit of the original filing date for the new matter.

The patent application must:

1. contain a written description of your invention and the manner and process of making and using it;
2. describe your invention in sufficient detail to enable one skilled in the art to make and use your invention; and
3. disclose the best mode contemplated by you of carrying out your invention.

Specifically, if you plan to modify, improve or add features to the invention which are not disclosed in the draft application, these modifications, improvements and features also need to be disclosed in the application as filed. You should also immediately notify us of your

Om P. Agrawal
Lattice Semiconductor Corp.
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planned date for making your invention public (e.g., using your invention publicly in a product, offering for sale a product made using or incorporating your invention, publishing a description of your invention, or providing samples of a product incorporating or made using your invention to a third party).

Additionally, we must disclose to the United States Patent and Trademark Office all information known by you to be material to the patentability of your invention. Such information is generally any prior art, any existing patents or patent applications relating to or similar to your invention, and any publication, sale, public use, offer to sell, public knowledge, or invention by others of your invention. If you are or become aware of such information, please provide the information to us.

Finally, if you plan to file this application in foreign countries, please note that some countries do not grant priority based on the filing date of a U.S. patent application. If you do not know whether one or more countries in which you intend to file the application grant such priority, please contact us immediately.

Please provide your comments on this application as soon as possible.

Very truly yours,


Greg J. Michelson

GJM/
Enclosure via email